

PATENT APPLICATION
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q79663

YAMADA, Junichi

Divisional of Appln. No. 10/211,378

Filed: August 5, 2002

Confirmation No.: 1428

Group Art Unit: 2818

Examiner: Ly D. PHAM

Appln. No.: Not yet assigned

Filed: February 9, 2004

Group Art Unit: not yet assigned

Confirmation No.: Not yet assigned

For: SEMICONDUCTOR MEMORY HAVING A DEFECTIVE MEMORY CELL
RELIEVING CIRCUIT

PRELIMINARY AMENDMENT

MAIL STOP NON-FEE AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Prior to examination, please amend the above-identified application as follows on the accompanying pages.

TABLE OF CONTENTS

AMENDMENTS TO THE SPECIFICATION.....	2
AMENDMENTS TO THE CLAIMS	3
REMARKS	14